

150V, 75A N-CHANNEL POWER MOSFET

GENERAL DESCRIPTION

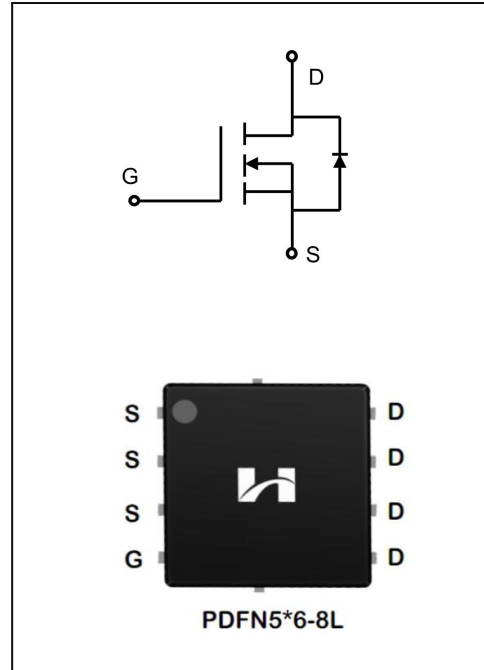
The SGM15HR11T uses advanced SGT technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety applications.

Features

- ◆ $V_{DS}=150V, I_D=75A$
- ◆ $R_{DS(on)}$
TYP: $9.5m\Omega @ V_{GS}=10V$

Applications

- ◆ Power faction correction (PFC)
- ◆ Switched mode power supplies (SMPS)
- ◆ Uninterruptible power supply (UPS)
- ◆ LED lighting power



ORDERING INFORMATION

| Part No. | Package | Marking | Material | Packing |
|------------|------------|------------|----------|---------|
| SGM15HR11T | PDFN5*6-8L | SGM15HR11T | Pb Free | Reel |

ABSOLUTE MAXIMUM RATINGS (T_J=25°C unless otherwise noted)

| Characteristics | | Symbol | Ratings | Unit |
|---|------------------------|------------------|----------|------|
| Drain-Source Voltage | | V _{DS} | 150 | V |
| Gate-Source Voltage | | V _{GS} | ±20 | V |
| Drain Current | T _C = 25°C | I _D | 75 | A |
| | T _C = 100°C | | 48 | |
| Drain Current Pulsed(Note 1) | | I _{DM} | 300 | A |
| Power Dissipation(T _C =25°C) -Derate above 25°C | | P _D | 125 | W |
| | | | 1.0 | W/°C |
| Single Pulsed Avalanche Energy (Note 2) | | E _{AS} | 42 | mJ |
| Operation Junction Temperature Range | | T _J | -55~+150 | °C |
| Storage Temperature Range | | T _{stg} | -55~+150 | °C |
| Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds | | TL | 260 | °C |

THERMAL CHARACTERISTICS

| Characteristics | Symbol | MAX | Unit |
|---|------------------|-----|------|
| Thermal Resistance, Junction-to-Case | R _{θJC} | 1.0 | °C/W |
| Thermal Resistance, Junction-to-Ambient | R _{θJA} | 50 | |

ELECTRICAL CHARACTERISTICS

| Characteristics | Symbol | Test conditions | Min. | Typ. | Max. | Unit |
|--|---------------------|---|------|------|------|------|
| Off Characteristics | | | | | | |
| Drain -Source Breakdown Voltage | B _{VDS} | V _{GS} =0V, I _D =250μA | 150 | -- | -- | V |
| Drain-Source Leakage Current | I _{DSS} | V _{DS} =150V, V _{GS} =0V | -- | -- | 1.0 | uA |
| Gate-Source Leakage Current | I _{GSS} | V _{GS} =20V, V _{DS} =0V | -- | -- | 100 | nA |
| Gate-Source Leakage Current | I _{GSS} | V _{GS} =-20V, V _{DS} =0V | -- | -- | -100 | |
| On Characteristics | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | V _{GS} = V _{DS} , I _D =250μA | 3.0 | 3.6 | 4.6 | V |
| Static Drain- Source On State Resistance | R _{DS(on)} | V _{GS} =10V, I _D =38A | -- | 9.5 | 11 | mΩ |
| Dynamic Characteristics | | | | | | |
| Gate Resistance | R _g | V _{GS} =0V; f=1.0MHZ | -- | 1.5 | -- | Ω |
| Input Capacitance | C _{iss} | V _{DS} =75V V _{GS} =0V f=1.0MHZ | -- | 2180 | -- | pF |
| Output Capacitance | C _{oss} | | -- | 522 | -- | |
| Reverse Transfer Capacitance | C _{rss} | | -- | 18 | -- | |
| Switching Characteristics | | | | | | |
| Turn-on Delay Time | t _{d(on)} | V _{DD} =75V, V _{GS} =10V R _G =3Ω, I _D =38A (Note 3.4) | -- | 21.5 | -- | ns |
| Turn-on Rise Time | t _r | | -- | 95.2 | -- | |
| Turn-off Delay Time | t _{d(off)} | | -- | 21.6 | -- | |
| Turn-off Fall Time | t _f | | -- | 43.5 | -- | |

| | | | | | | |
|--------------------|----------|---|----|-----|----|----|
| Total Gate Charge | Q_g | $V_{DS}=75V, I_D=38A$ $V_{GS}=10V$ (Note 3.4) | -- | 33 | -- | nc |
| Gate-Source Charge | Q_{gs} | | -- | 20 | -- | |
| Gate-Drain Charge | Q_{gd} | | -- | 6.3 | -- | |

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

| Characteristics | Symbol | Test conditions | Min. | Typ. | Max. | Unit |
|---------------------------|----------|---|------|------|------|------|
| Continuous Source Current | I_S | Integral Reverse P-N Junction Diode in the MOSFET | -- | -- | 75 | A |
| Pulsed Source Current | I_{SM} | | -- | -- | 300 | |
| Diode Forward Voltage | V_{SD} | $I_S=38A, V_{GS}=0V$ | -- | -- | 1.2 | V |
| Reverse Recovery Time | T_{rr} | $I_F=38A, V_R=10V,$ $dIF/dt=100A/\mu S$ | -- | 46 | -- | ns |
| Reverse Recovery Charge | Q_{rr} | | -- | 47 | -- | nC |

1. Pulse width limited by maximum junction temperature
2. $L=0.1mH, V_{DD}=80V, V_G=10V, R_G=25\Omega,$ starting $T_J=25^\circ C$
3. Pulse Test: Pulse width $\leq 300\mu s,$ Duty cycle $\leq 2\%$
4. Essentially independent of operating temperature

Typical Performance Characteristics

Figure 1. Output Characteristics

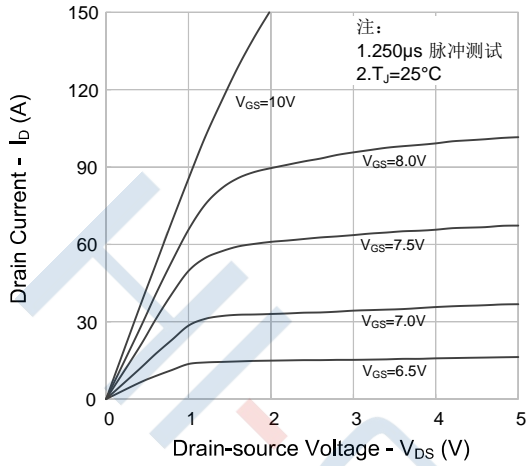


Figure 2. Transfer Characteristics

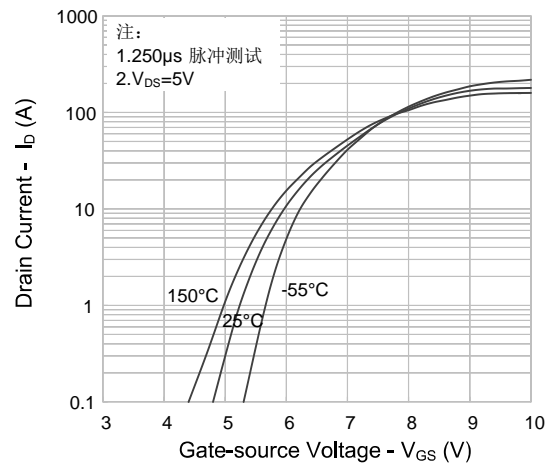


Figure 3. On-resistance vs. Drain Current

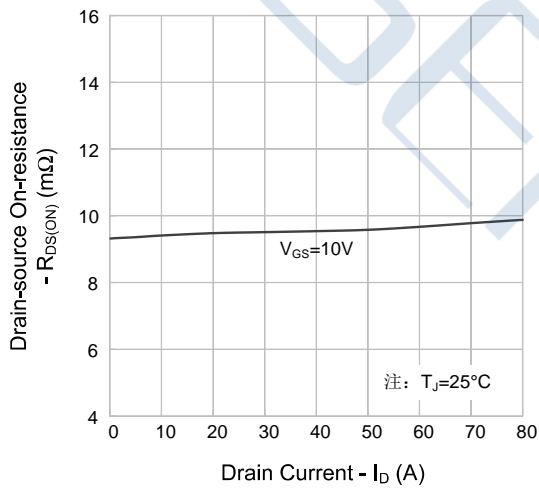


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

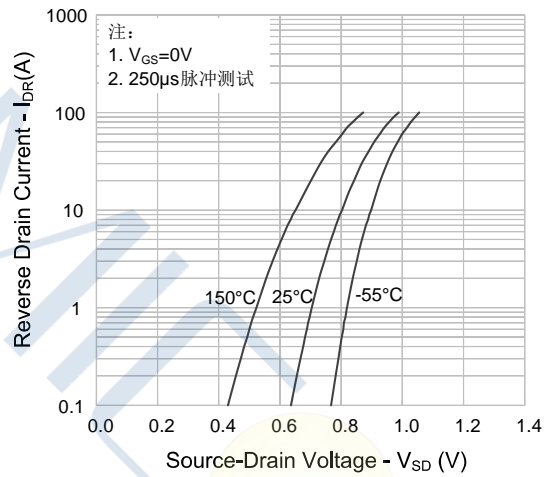


Figure 5. Capacitance Characteristics

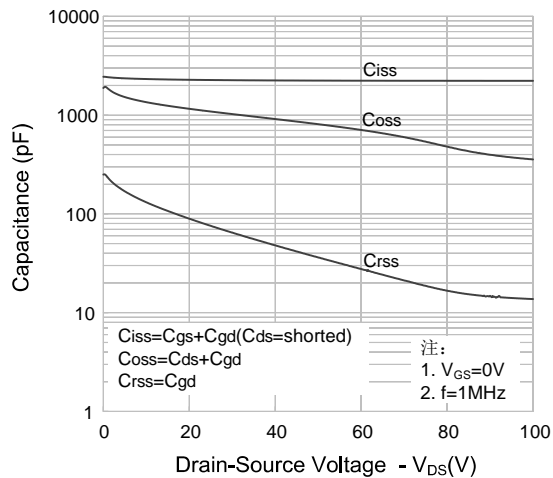
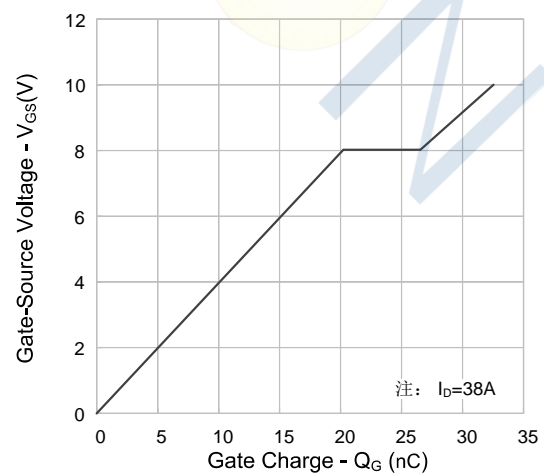


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics

Figure 7. Breakdown Voltage vs. Temperature Characteristics

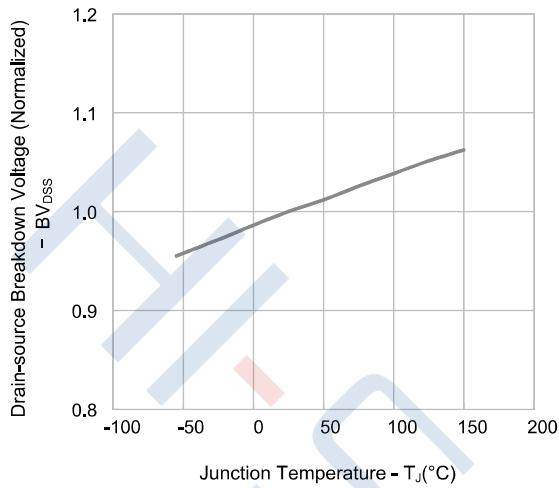


Figure 8. On-resistance vs. Temperature Characteristics

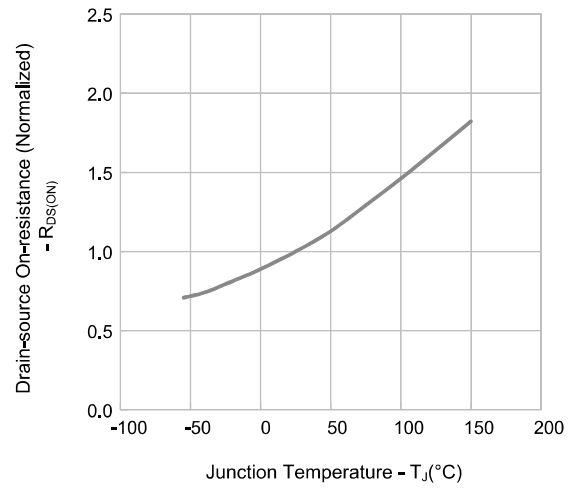
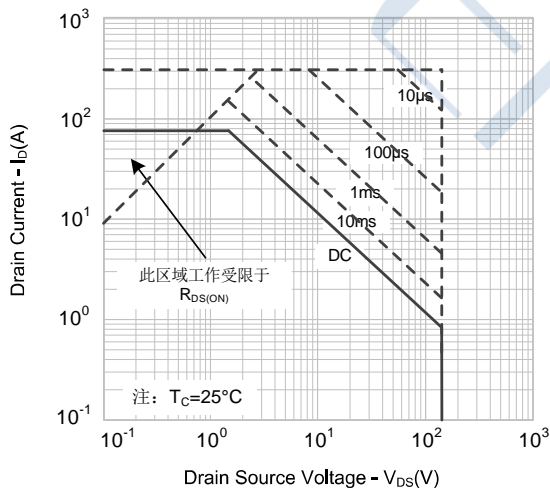
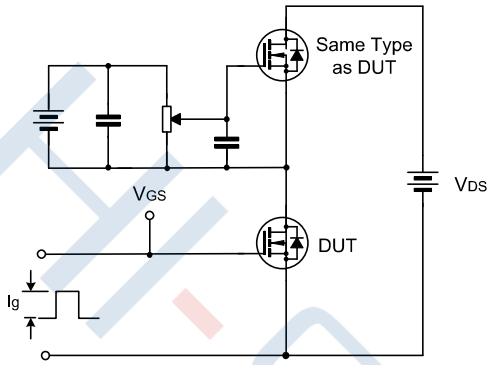


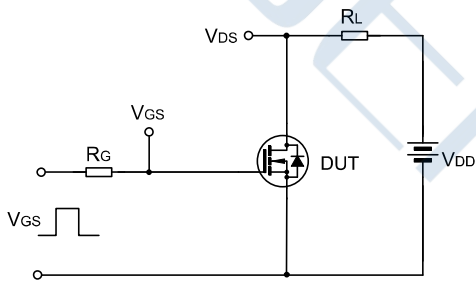
Figure 9. Max. Safe Operating Area



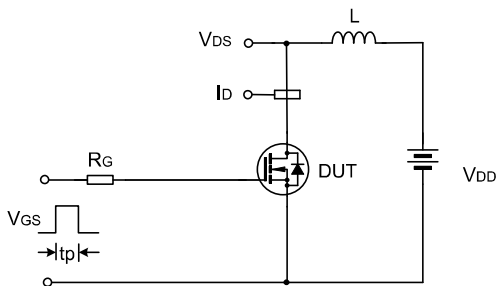
Test Circuit



Gate Charge Test Circuit & Waveform

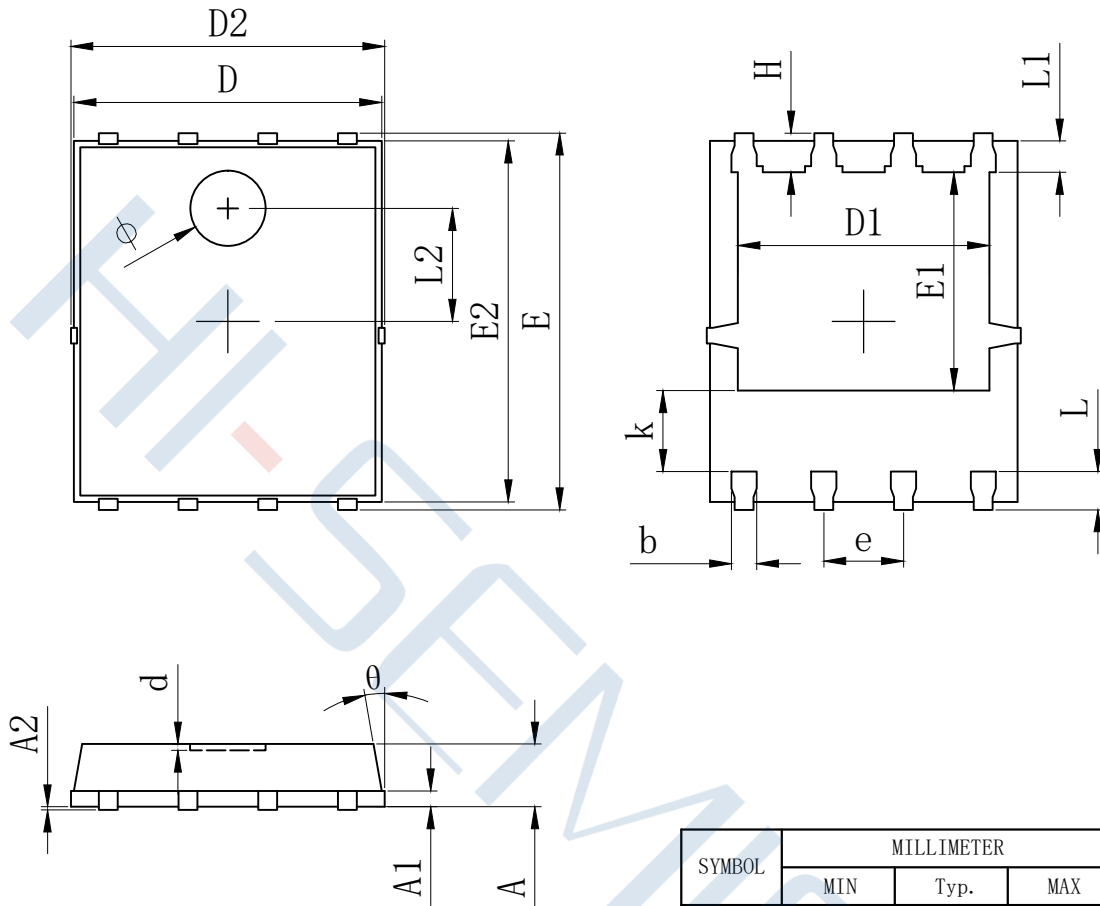


Resistive Switching Test Circuit & Waveform



Unclamped Inductive Switching Test Circuit & Waveform

Package Dimensions of PDFN5*6-8L



| SYMBOL | MILLIMETER | | |
|----------|---------------------|-------|-------|
| | MIN | Typ. | MAX |
| A | 0.900 | 1.000 | 1.100 |
| A1 | 0.254 REF. | | |
| A2 | 0 [~] 0.05 | | |
| D | 4.824 | 4.900 | 4.976 |
| D1 | 3.910 | 4.010 | 4.110 |
| D2 | 4.924 | 5.000 | 5.076 |
| E | 5.924 | 6.000 | 6.076 |
| E1 | 3.375 | 3.475 | 3.575 |
| E2 | 5.674 | 5.750 | 5.826 |
| b | 0.350 | 0.400 | 0.450 |
| e | 1.270 TYP. | | |
| L | 0.534 | 0.610 | 0.686 |
| L1 | 0.424 | 0.500 | 0.576 |
| L2 | 1.800 REF. | | |
| k | 1.190 | 1.290 | 1.390 |
| H | 0.549 | 0.625 | 0.701 |
| θ | 8° | 10° | 12° |
| ϕ | 1.100 | 1.200 | 1.300 |
| d | | | 0.100 |

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