

60V, 80A N-CHANNEL POWER MOSFET

GENERAL DESCRIPTION

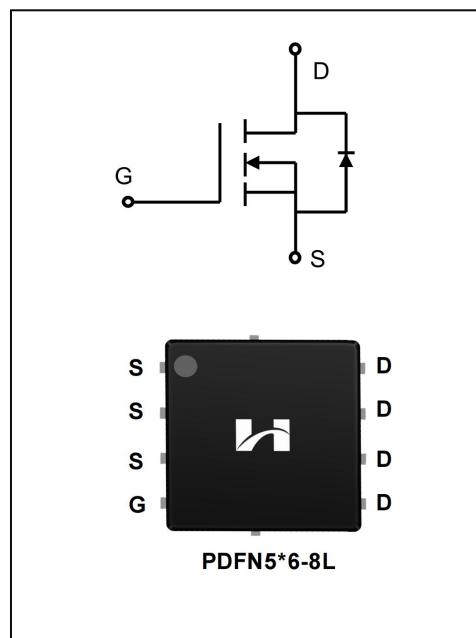
The SFM6008T uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety applications.

Features

- ◆ $V_{DS}=60V, I_D=80A$
 - ◆ $R_{DS(on)}$
- TYP: $6.3m\Omega @ V_{GS}=10V, I_D=30A$

Applications

- ◆ Power factor correction (PFC)
- ◆ Switched mode power supplies (SMPS)
- ◆ Uninterruptible power supply (UPS)
- ◆ LED lighting power



ORDERING INFORMATION

Part No.	Package	Marking	Material	Packing
SFM6008T	PDFN5*6-8L	SFM6008T	Pb Free	Reel

ABSOLUTE MAXIMUM RATINGS ($T_J=25^\circ\text{C}$ unless otherwise noted)

Characteristics	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	
Drain Current	I_D	80	A
		55	
	I_{DM}	320	
Power Dissipation($T_C=25^\circ\text{C}$) -Derate above 25°C	P_D	93	W
		0.86	W/ $^\circ\text{C}$
	E_{AS}	336	mJ
Single Pulsed Avalanche Energy (Note 2)	T_J	-55~+150	$^\circ\text{C}$
Operation Junction Temperature Range	T_{stg}	-55~+150	
Storage Temperature Range	T_L	300	
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds			

THERMAL CHARACTERISTICS

Characteristics	Symbol	MAX	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.8	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	50	

ELECTRICAL CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
Drain -Source Breakdown Voltage	B_{VDS}	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	60	--	--	V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=60\text{V}, V_{GS}=0\text{V}$	--	--	1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=20\text{V}, V_{DS}=0\text{V}$	--	--	100	nA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=-20\text{V}, V_{DS}=0\text{V}$	--	--	-100	
On Characteristics						
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{GS}=V_{DS}, I_D=250\mu\text{A}$	1.0	1.5	2.0	V
Static Drain- Source On State Resistance	$R_{DS(\text{on})}$	$V_{GS}=10\text{V}, I_D=30\text{A}$	--	6.3	7.5	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=20\text{A}$	--	7.2	9.5	
Dynamic Characteristics						
Gate Resistance	R_g	$V_{GS}=0\text{V}, f=1.0\text{MHz}$	1	2.8	10	Ω
Input Capacitance	C_{iss}	$V_{DS}=25\text{V}$	--	6520	--	pF
Output Capacitance	C_{oss}		--	246	--	
Reverse Transfer Capacitance	C_{rss}		f=1.0MHz	--	133	
Switching Characteristics						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=30\text{V}, V_{GS}=10\text{V}$ $R_G=3\Omega, I_D=30\text{A}$ (Note 3.4)	--	18.3	--	ns
Turn-on Rise Time	t_r		--	45.6	--	
Turn-off Delay Time	$t_{d(off)}$		--	85.5	--	
Turn-off Fall Time	t_f		--	23.7	--	

Total Gate Charge	Q_g	$V_{DS}=30V, I_D=30A$ $V_{GS}=10V$ (Note 3.4)	--	102.1	--	nc
Gate-Source Charge	Q_{gs}		--	19.8	--	
Gate-Drain Charge	Q_{gd}		--	41.5	--	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Continuous Source Current	I_s	Integral Reverse P-N Junction Diode in the MOSFET	--	--	80	A
Pulsed Source Current	I_{SM}		--	--	320	
Diode Forward Voltage	V_{SD}	$I_s=30A, V_{GS}=0V$	--	0.8	1.2	V
Reverse Recovery Time	T_{rr}	$I_F=30A$ $dI/dt=100A/\mu s$	--	42	--	ns
Reverse Recovery Charge	Q_{rr}		--	48	--	nC

1. Pulse width limited by maximum junction temperature

2. $L=1mH, V_{DD}=30V, V_G=10V, R_G=25\Omega$, starting $T_J=25^\circ C$ 3. Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$

4. Essentially independent of operating temperature

Typical Performance Characteristics

Figure 1. Output Characteristics

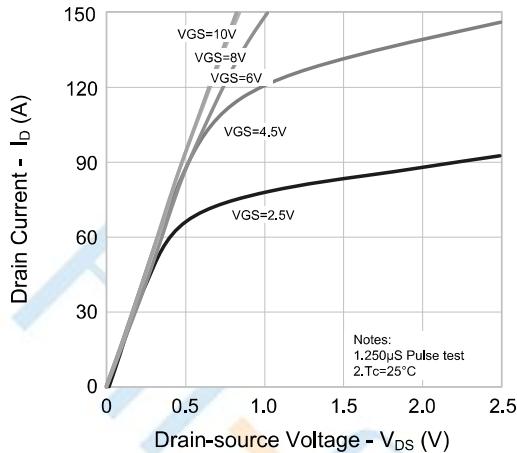


Figure 2. Transfer Characteristics

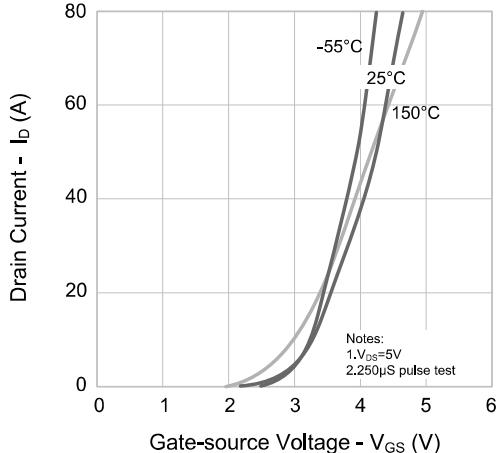


Figure 3. On-resistance vs. Drain Current

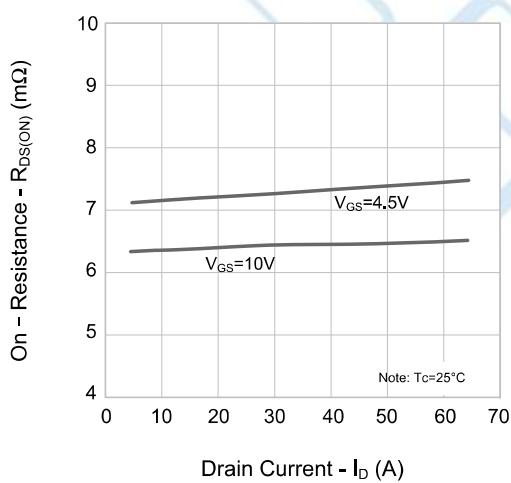


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

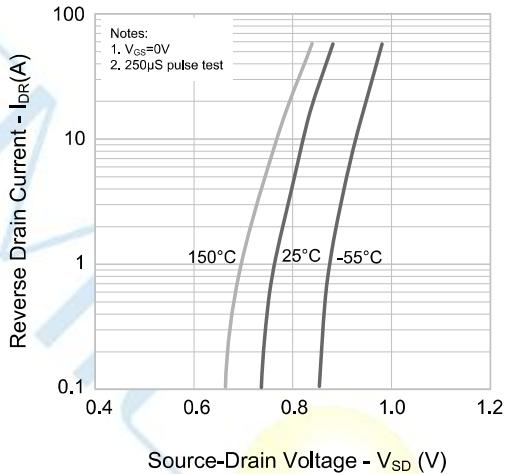


Figure 5. Capacitance Characteristics

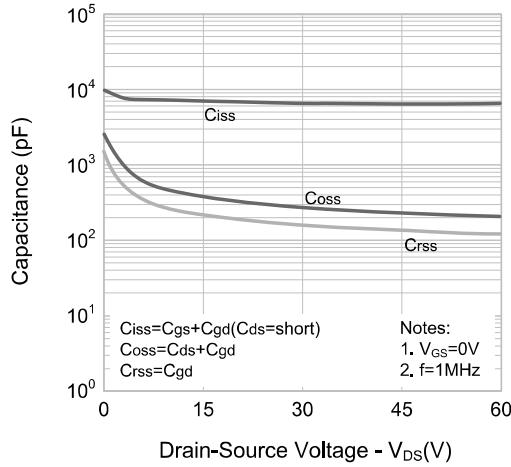
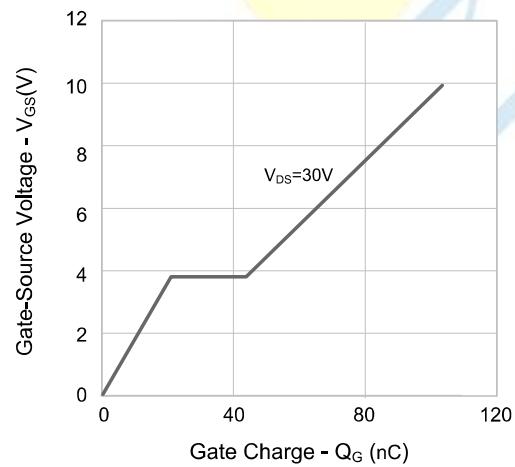
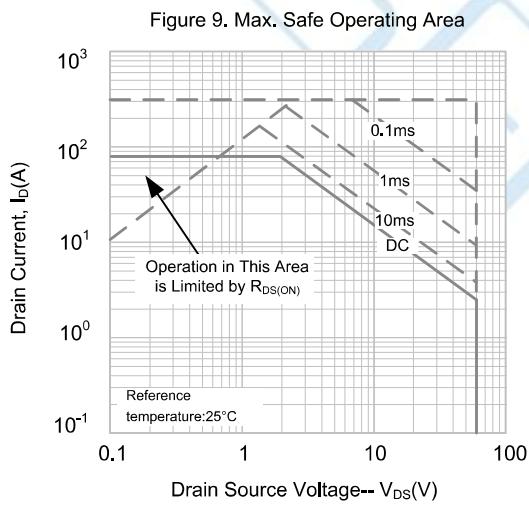
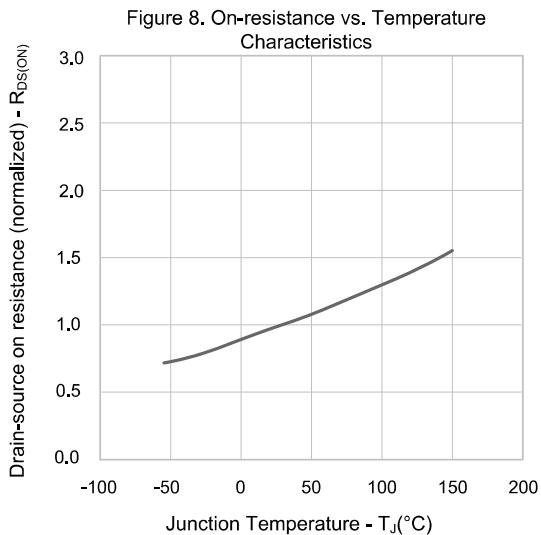
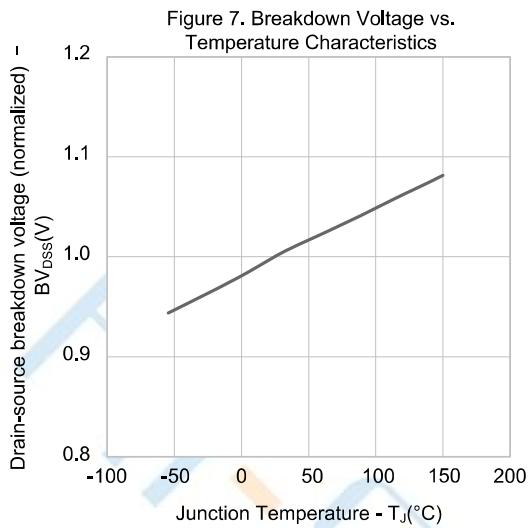


Figure 6. Gate Charge



Typical Performance Characteristics



Test Circuit

Figure 1: Gate Charge Test Circuit & Waveform

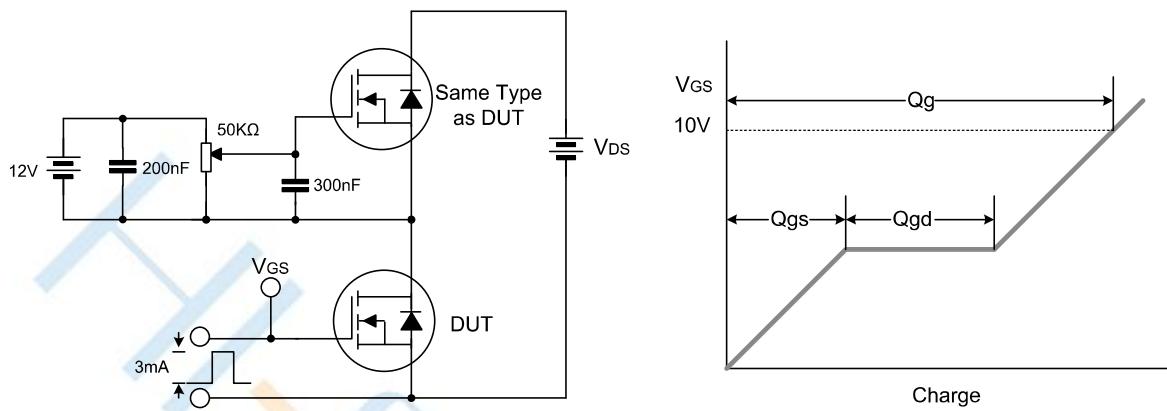


Figure 2: Resistive Switching Test Circuit & Waveform

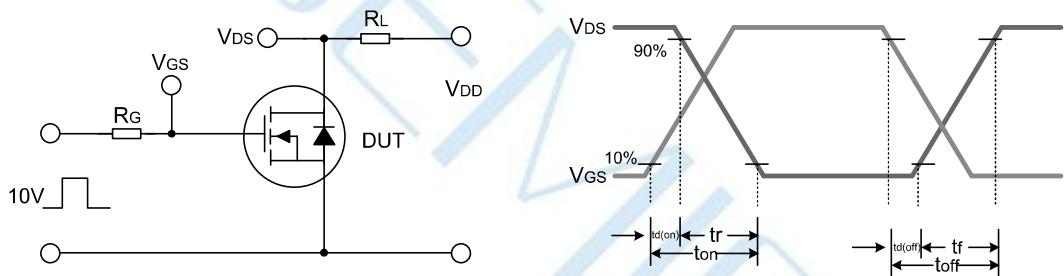
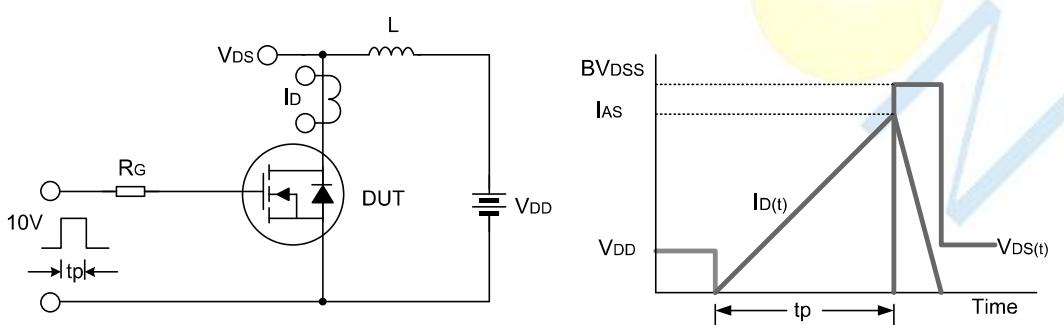
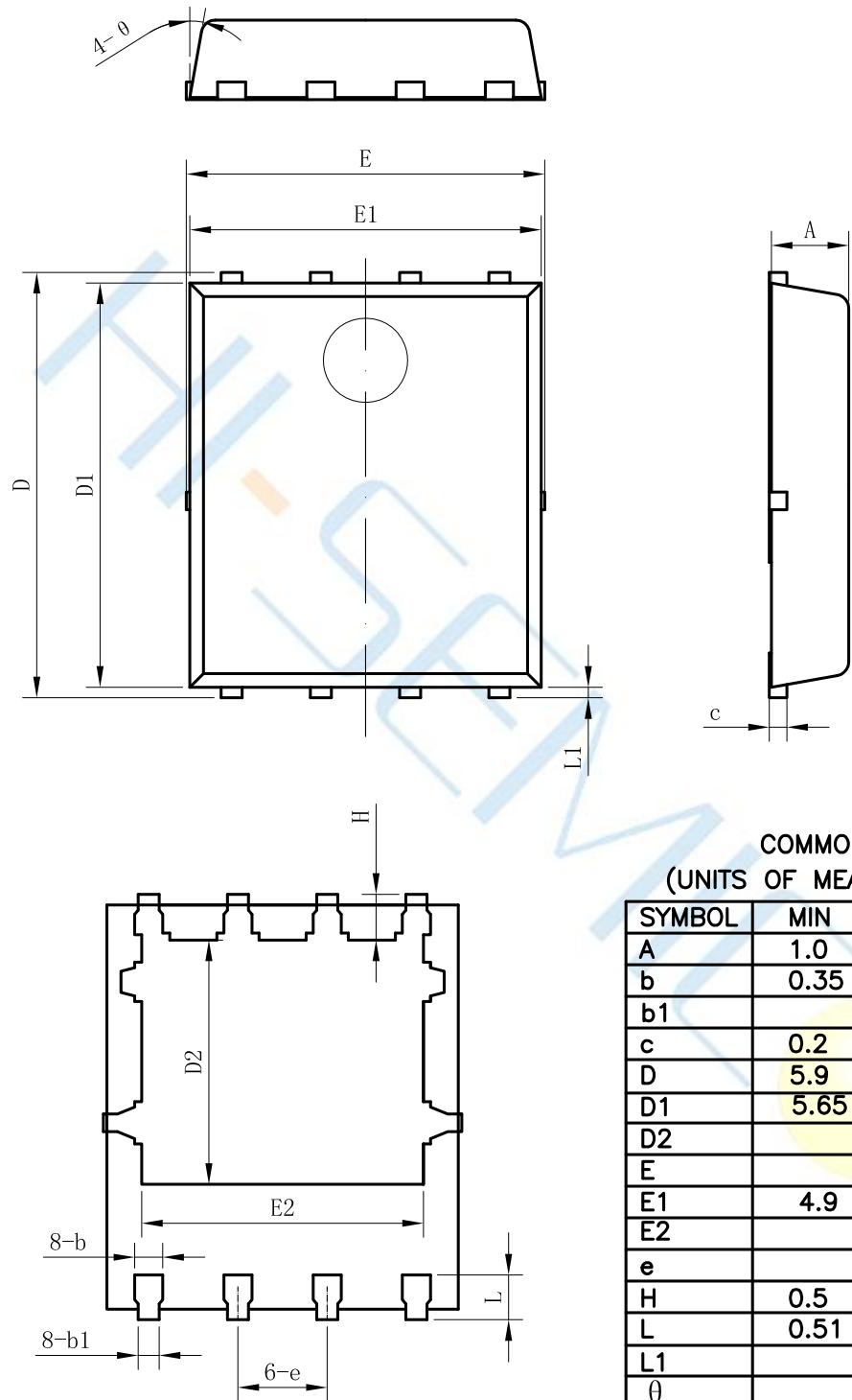


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform



Package Dimensions of PDFN5*6-8L

Unit:mm



Disclaimer:

- Hi-semicon reserves the right to make changes to the information herein for the improvement of the design and performance without further notice! Customers should obtain the latest relevant information before placing orders and should verify that such information is complete and current.
- All semiconductor products malfunction or fail with some probability under special conditions. When using Hi-semicon products in system design or complete machine manufacturing, it is the responsibility of the buyer to comply with the safety standards strictly and take essential measures to avoid situations in which a malfunction or failure of such Hi-semicon products could cause loss of body injury or damage to property.
- Hi-semicon will supply the best possible product for customers!

