

40V, 130A N-CHANNEL POWER MOSFET

GENERAL DESCRIPTION

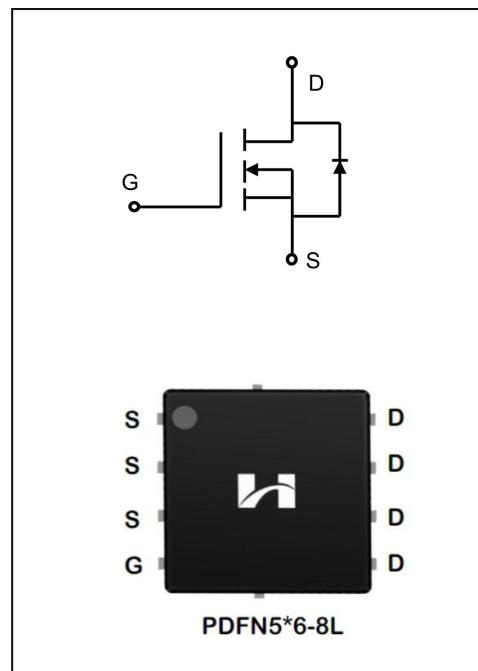
The SGM042R1T uses advanced SGT technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety applications.

Features

- ◆ $V_{DS}=40V, I_D=130A$
- ◆ $R_{DS(on)}$
TYP: $1.8m\Omega @ V_{GS}=10V$

Applications

- ◆ Power factor correction (PFC)
- ◆ Switched mode power supplies (SMPS)
- ◆ Uninterruptible power supply (UPS)
- ◆ LED lighting power



ORDERING INFORMATION

Part No.	Package	Marking	Material	Packing
SGM042R1T	PDFN5*6-8L	SGM042R1T	Pb Free	Reel

ABSOLUTE MAXIMUM RATINGS ($T_J=25^\circ\text{C}$ unless otherwise noted)

Characteristics		Symbol	Ratings		Unit
Drain-Source Voltage		V_{DS}	40		V
Gate-Source Voltage		V_{GS}	± 20		
Drain Current	$T_C = 25^\circ\text{C}$	I_D	130		A
	$T_C = 100^\circ\text{C}$		83		
Drain Current Pulsed(Note 1)		I_{DM}	520		
Power Dissipation($T_C=25^\circ\text{C}$) -Derate above 25°C		P_D	93		W
Single Pulsed Avalanche Energy (Note 2)		E_{AS}	105		mJ
Operation Junction Temperature Range		T_J	-55~+150		$^\circ\text{C}$
Storage Temperature Range		T_{stg}	-55~+150		
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		TL	300		

THERMAL CHARACTERISTICS

Characteristics	Symbol	MAX		Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.35		
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	50		$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
Drain -Source Breakdown Voltage	B_{VDSS}	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	40	--	--	V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=40\text{V}, V_{GS}=0\text{V}$	--	--	1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=20\text{V}, V_{DS}=0\text{V}$	--	--	100	nA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=-20\text{V}, V_{DS}=0\text{V}$	--	--	-100	
On Characteristics						
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{GS}=V_{DS}, I_D=250\mu\text{A}$	1.4	1.75	2.4	V
Static Drain- Source On State Resistance	$R_{DS(\text{on})}$	$V_{GS}=10\text{V}, I_D=50\text{A}$	--	1.8	2.1	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=50\text{A}$	--	2.8	3.5	
Dynamic Characteristics						
Gate Resistance	R_g	$V_{GS}=0\text{V}, f=1.0\text{MHz}$	1	0.9	3	Ω
Input Capacitance	C_{iss}	$V_{DS}=25\text{V}$	--	2938	--	pF
Output Capacitance	C_{oss}		--	754	--	
Reverse Transfer Capacitance	C_{rss}	$f=1.0\text{MHz}$	--	16.2	--	
Switching Characteristics						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=20\text{V}, V_{DS}=10\text{V}$ $R_G=4.7\Omega, I_D=50\text{A}$	--	11	--	ns
Turn-on Rise Time	t_r		--	58	--	
Turn-off Delay Time	$t_{d(off)}$		--	42	--	

Turn-off Fall Time	t_f	$V_{DD}=20V, V_{DS}=10V$ $R_G=4.7\Omega, I_D=50A$ (Note 3.4)	--	12	--	ns
Total Gate Charge	Q_g	$V_{DD}=20V, V_{GS}=10V$ $I_D=50A$ (Note 3.4)	--	42	--	nc
Gate-Source Charge	Q_{gs}		--	13	--	
Gate-Drain Charge	Q_{gd}		--	6.5	--	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Continuous Source Current	I_s	Integral Reverse P-N Junction Diode in the MOSFET	--	--	130	A
Pulsed Source Current	I_{SM}		--	--	520	
Diode Forward Voltage	V_{SD}	$I_s=50A, V_{GS}=0V$	--	0.85	1.2	V
Reverse Recovery Time	T_{rr}	$I_f=25A, V_R=20V,$	--	47	--	ns
Reverse Recovery Charge	Q_{rr}	$dI/dt=100A/\mu s$	--	45	--	nC

1.Pulse width limited by maximum junction temperature

2. $L=0.1mH, V_{DD}=32V, V_G=10V, R_G=25\Omega$, starting $T_J=25^\circ C$ 3.Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$

4.Essentially independent of operating temperature

Typical Performance Characteristics

Figure 1. On-Region Characteristics

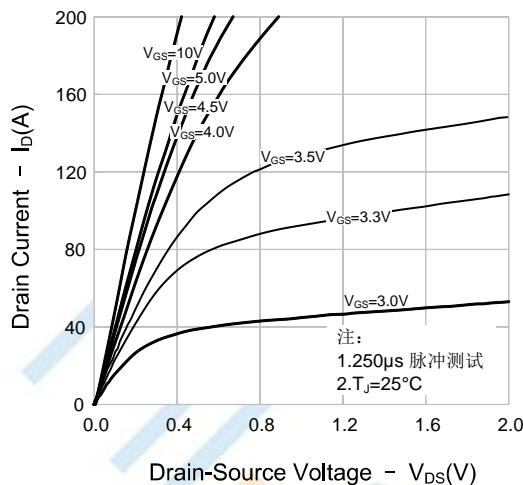


Figure 2. Transfer Characteristics

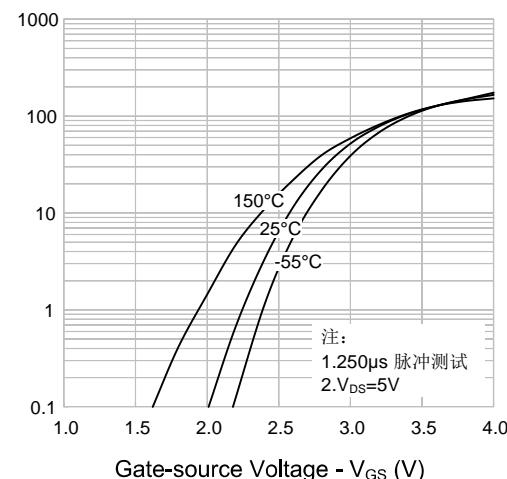


Figure 3. On-resistance vs. Drain Current

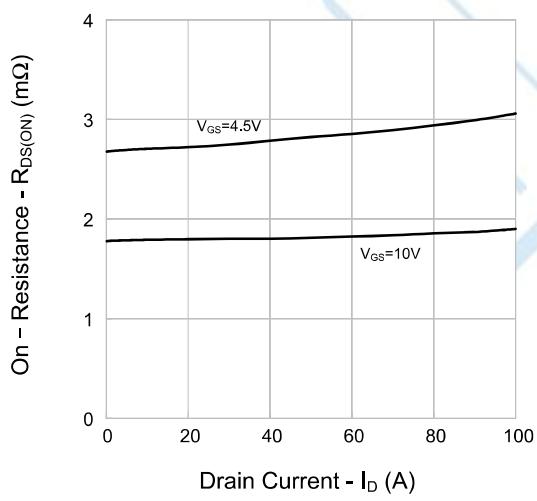


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

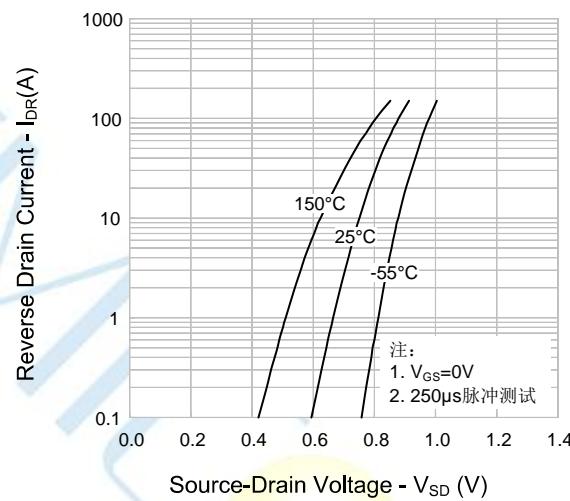


Figure 5. Capacitance Characteristics

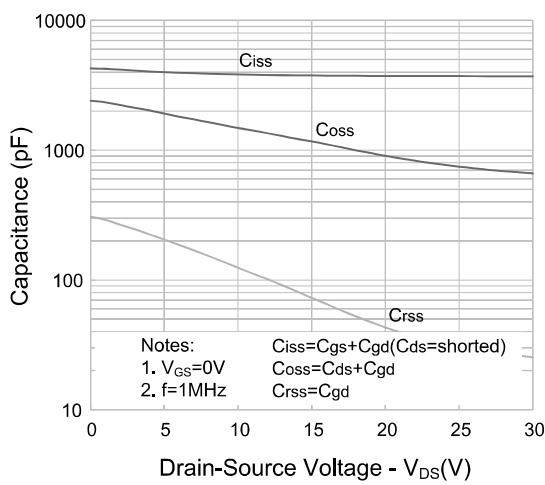
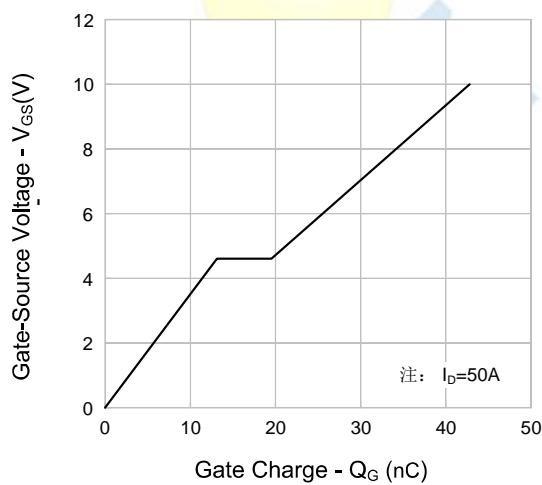


Figure 6. Gate Charge



Typical Performance Characteristics

Figure 7. Breakdown Voltage Variation vs. Temperature

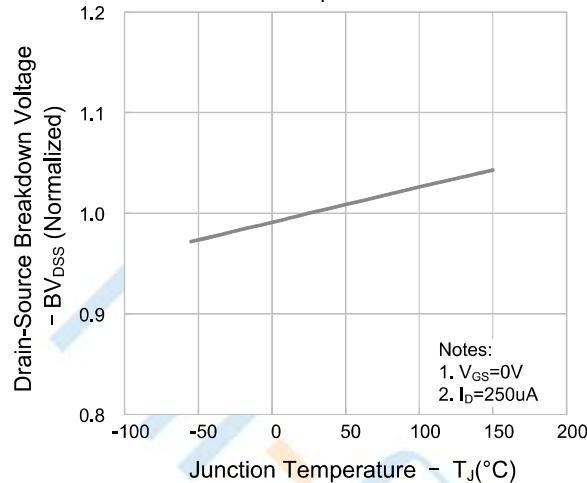


Figure 8. On-resistance Variation vs. Temperature

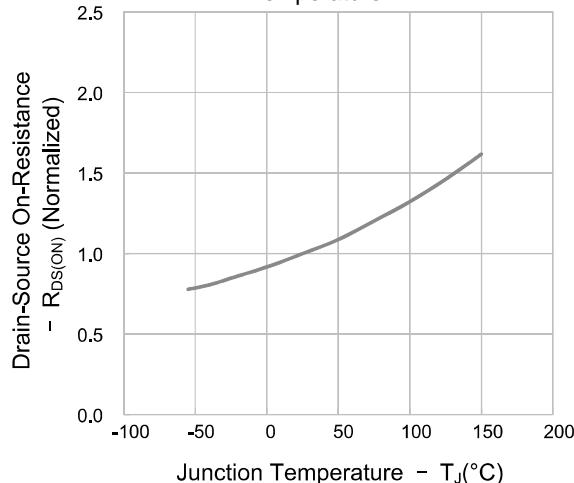
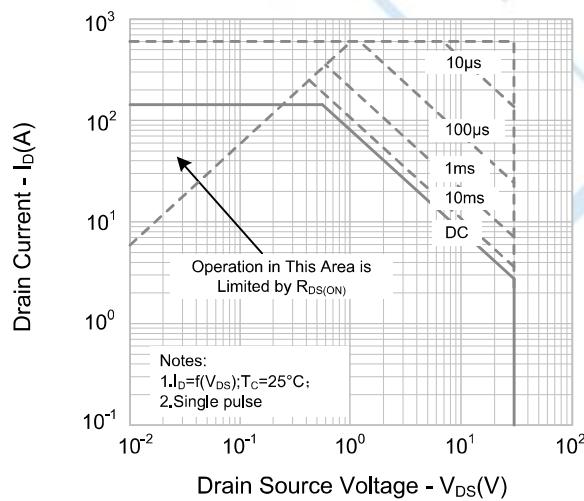
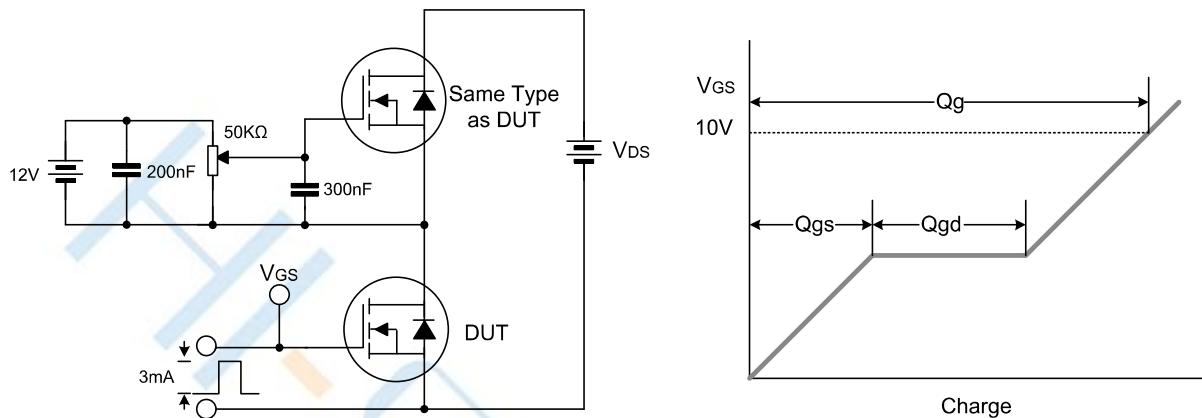


Figure 9 . Max. Safe Operating Area

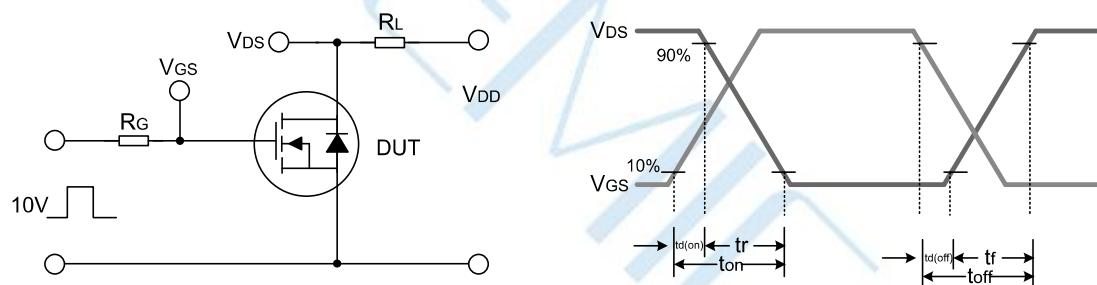


Test Circuit

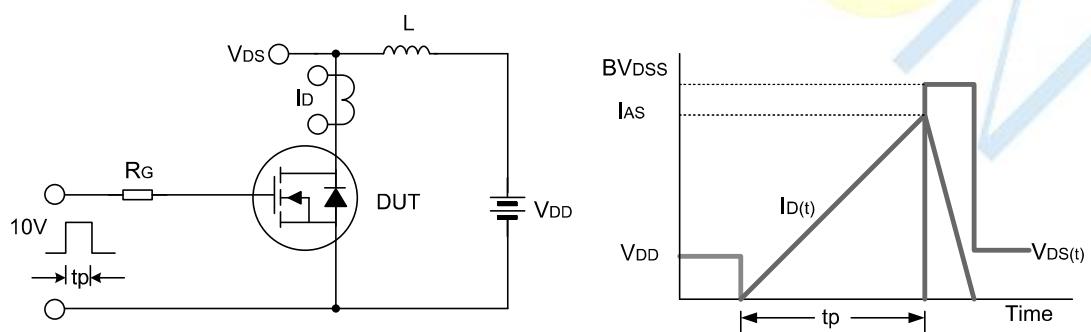
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform

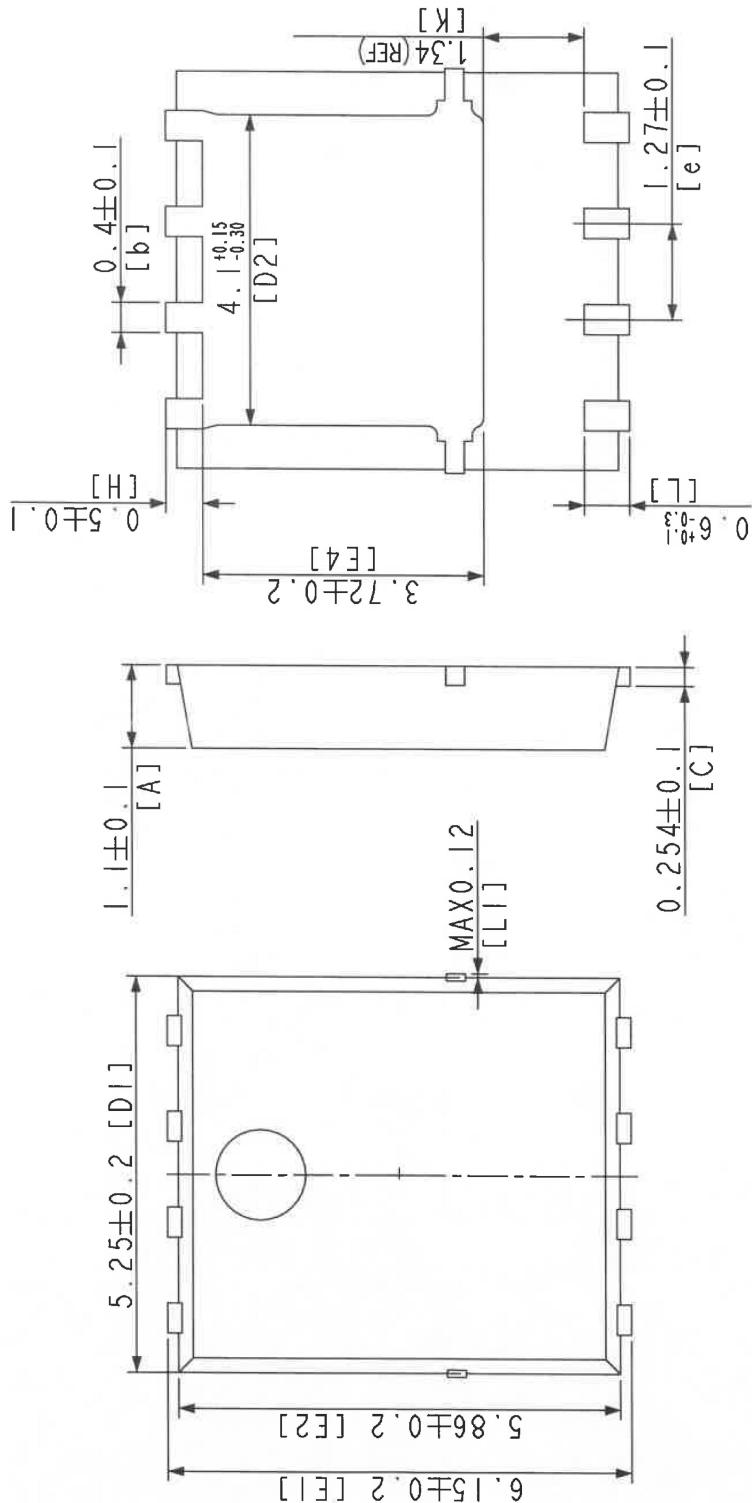


Unclamped Inductive Switching Test Circuit & Waveform



Package Dimensions of PDFN5*6-8L

Unit :mm



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