

## 40V, 50A DUAL N CHANNEL POWER MOSFET

### GENERAL DESCRIPTION

The SFM4005DT uses advanced trench technology and design to provide excellent  $R_{DS(on)}$  with low gate charge.

It can be used in a wide variety applications.

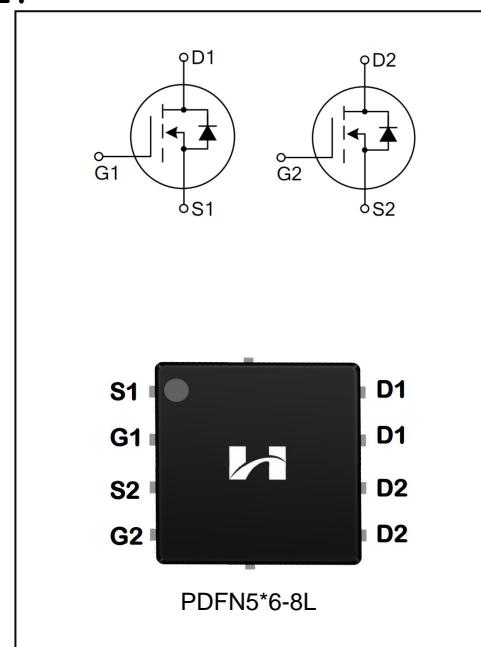
### Features

- ◆  $V_{DS}=40V$ ,  $I_D=50A$
- ◆  $R_{DS(on)}$

TYP:19m $\Omega$ @ $V_{GS}=10V$

### Applications

- ◆ Power factor correction (PFC)
- ◆ Switched mode power supplies (SMPS)
- ◆ Uninterruptible power supply (UPS)
- ◆ LED lighting power



### ORDERING INFORMATION

| Part No.  | Package | Marking   | Material | Packing |
|-----------|---------|-----------|----------|---------|
| SFM4005DT | PDFN5*6 | SFM4005DT | Pb Free  | Reel    |

**ABSOLUTE MAXIMUM RATINGS (T<sub>J</sub>=25°C unless otherwise noted)**

| Characteristics   | Symbol           | Ratings  | Unit |
|---|------------------|----------|------|
| Drain-Source Voltage  | V <sub>DS</sub>  | 40       | V    |
| Gate-Source Voltage   | V <sub>GS</sub>  | ±20      | V    |
| Drain Current   | I <sub>D</sub>   | 50       | A    |
| T <sub>C</sub> = 25°C   |                  | 35       |      |
| T <sub>C</sub> = 100°C  |                  |          |      |
| Drain Current Pulsed(Note 1)  | I <sub>DM</sub>  | 150      | A    |
| Power Dissipation(T <sub>C</sub> =25°C)<br>-Derate above 25°C                 | P <sub>D</sub>   | 30       | W    |
|   |                  | 0.37     | W/°C |
|   |                  |          |      |
| Operation Junction Temperature Range  | T <sub>J</sub>   | -55~+150 | °C   |
| Storage Temperature Range   | T <sub>stg</sub> | -55~+150 | °C   |
| Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds | TL               | 300      | °C   |

**THERMAL CHARACTERISTICS**

| Characteristics                         | Symbol           | MAX | Unit |
|---|------------------|-----|------|
| Thermal Resistance, Junction-to-Case    | R <sub>θJC</sub> | 1.8 | °C/W |
| Thermal Resistance, Junction-to-Ambient | R <sub>θJA</sub> | 50  | °C/W |

**ELECTRICAL CHARACTERISTICS**

| Characteristics                          | Symbol              | Test conditions  | Min. | Typ. | Max. | Unit |
|--|---------------------|--|------|------|------|------|
| <b>Off Characteristics</b>               |                     |  |      |      |      |      |
| Drain -Source Breakdown Voltage          | B <sub>VDSS</sub>   | V <sub>GS</sub> =0V, I <sub>D</sub> =250μA   | 40   | --   | --   | V    |
| Drain-Source Leakage Current             | I <sub>DSS</sub>    | V <sub>DS</sub> =40V, V <sub>GS</sub> =0V  | --   | --   | 1    | uA   |
| Gate-Source Leakage Current              | I <sub>GSS</sub>    | V <sub>GS</sub> =20V, V <sub>DS</sub> =0V  | --   | --   | 100  | nA   |
| Gate-Source Leakage Current              | I <sub>GSS</sub>    | V <sub>GS</sub> =-20V, V <sub>DS</sub> =0V   | --   | --   | -100 |      |
| <b>On Characteristics</b>                |                     |  |      |      |      |      |
| Gate Threshold Voltage                   | V <sub>GS(th)</sub> | V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> =250μA  | 1.0  | 1.7  | 2.2  | V    |
| Static Drain- Source On State Resistance | R <sub>DS(on)</sub> | V <sub>GS</sub> =10V, I <sub>D</sub> =15A  | --   | 19   | 23   | mΩ   |
|  |                     | V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A   | --   | 24.5 | 35   |      |
| <b>Dynamic Characteristics</b>           |                     |  |      |      |      |      |
| Gate Resistance                          | R <sub>g</sub>      | V <sub>GS</sub> =0V, f=1.0MHZ  | 1    | 5    | 10   | Ω    |
| Input Capacitance                        | C <sub>iss</sub>    | V <sub>DS</sub> =20V<br>V <sub>GS</sub> =0V<br>f=1.0MHZ  | --   | 686  | --   | pF   |
| Output Capacitance                       | C <sub>oss</sub>    |  | --   | 75   | --   |      |
| Reverse Transfer Capacitance             | C <sub>rss</sub>    |  | --   | 66   | --   |      |
| <b>Switching Characteristics</b>         |                     |  |      |      |      |      |
| Turn-on Delay Time                       | t <sub>d(on)</sub>  | V <sub>DD</sub> =20V, V <sub>GS</sub> =10V<br>R <sub>GS</sub> =3.3Ω, I <sub>D</sub> =15A<br>(Note 2.3) | --   | 8.3  | --   | ns   |
| Turn-on Rise Time                        | t <sub>r</sub>      |  | --   | 6.5  | --   |      |
| Turn-off Delay Time                      | t <sub>d(off)</sub> |  | --   | 18.5 | --   |      |
| Turn-off Fall Time                       | t <sub>f</sub>      |  | --   | 5.1  | --   |      |

|                    |          |   |    |      |    |    |
|--------------------|----------|---|----|------|----|----|
| Total Gate Charge  | $Q_g$    | $V_{DS}=20V, I_D=15A$<br>$V_{GS}=10V$<br>(Note 2.3) | -- | 15.6 | -- | nc |
| Gate-Source Charge | $Q_{gs}$ |   | -- | 3.7  | -- |    |
| Gate-Drain Charge  | $Q_{gd}$ |   | -- | 4.6  | -- |    |

## SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

| Characteristics           | Symbol   | Test conditions   | Min. | Typ. | Max. | Unit |
|---------------------------|----------|---|------|------|------|------|
| Continuous Source Current | $I_s$    | Integral Reverse P-N<br>Junction Diode in the<br>MOSFET | --   | --   | 50   | A    |
| Pulsed Source Current     | $I_{SM}$ |   | --   | --   | 150  |      |
| Diode Forward Voltage     | $V_{SD}$ | $I_s=15A, V_{GS}=0V$                                    | --   | 0.85 | 1.2  | V    |

1. Pulse width limited by maximum junction temperature

2.  $L=1mH, V_{DD}=20V, V_G=10V, R_G=25\Omega$ , starting  $T_J=25^\circ C$ 3. Pulse Test: Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$ 

4. Essentially independent of operating temperature

## Typical Performance Characteristics

Figure 1.On-Region Characteristics

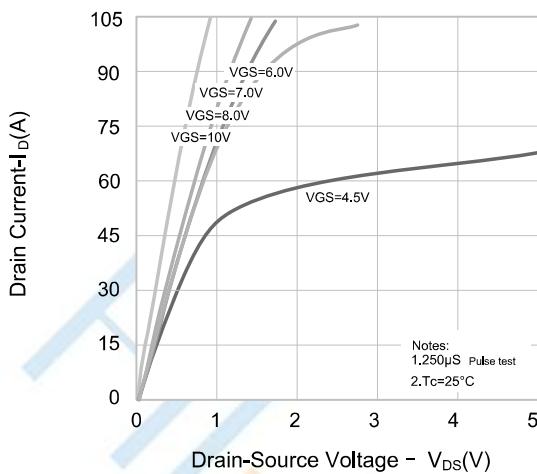


Figure 2.Transfer Characteristics

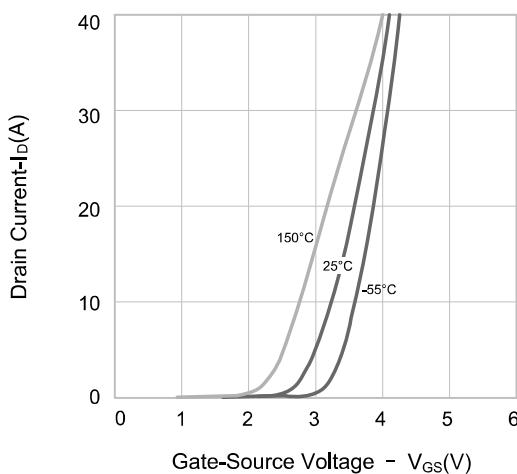


Figure 3.On-Resistance Variation vs. Drain-Current, Gate Voltage

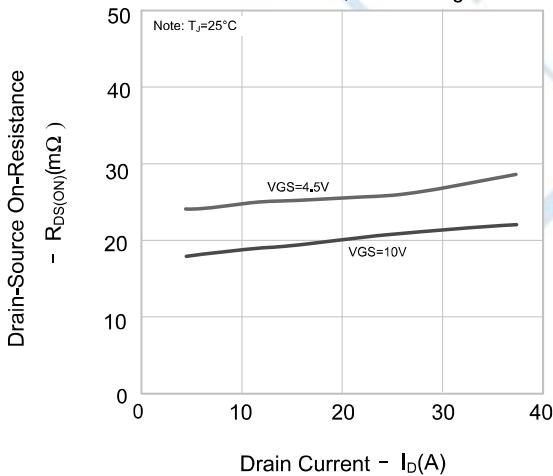


Figure 4.Body Diode Forward Voltage Variation vs.Source Current and Temperature

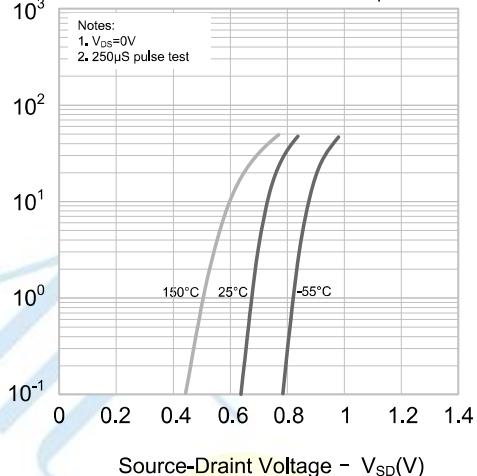


Figure 5.Capacitance Characteristics

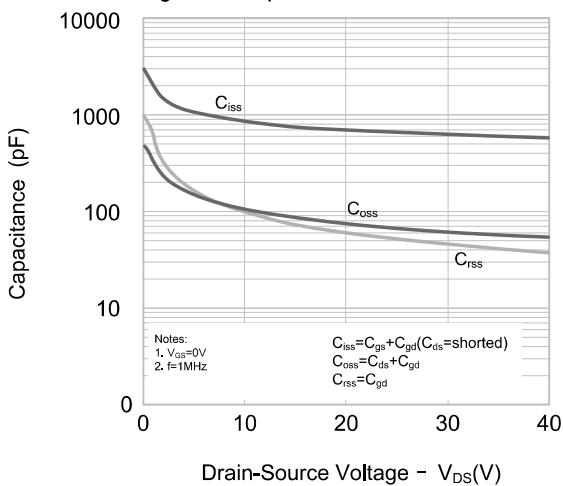
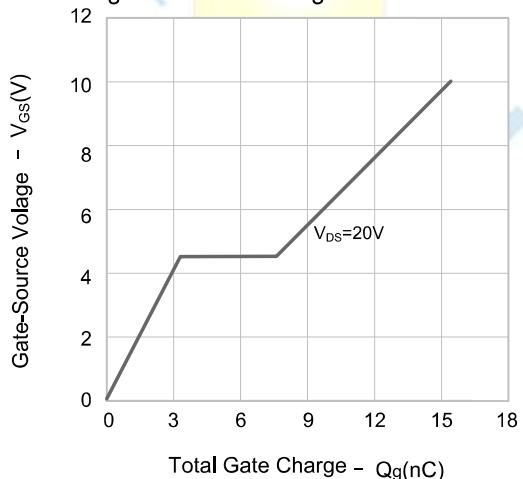


Figure 6.Gate Charge Characteristics



## Typical Performance Characteristics

Figure 7.Breakdown Voltage Variation  
vs.Temperature

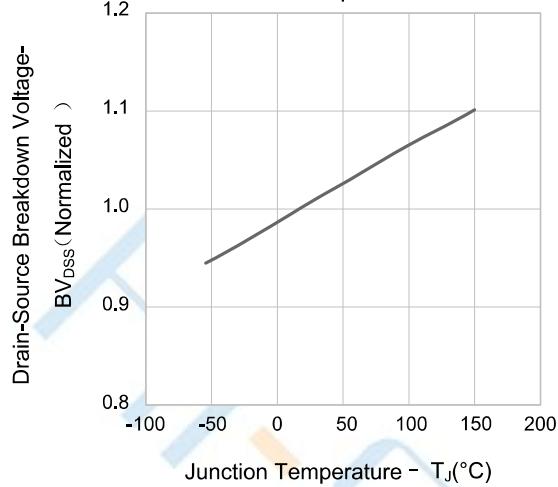


Figure 8.On-resistance Variation  
vs.Temperature

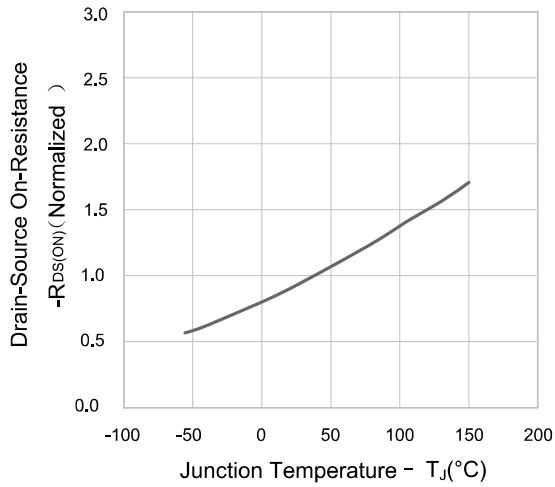
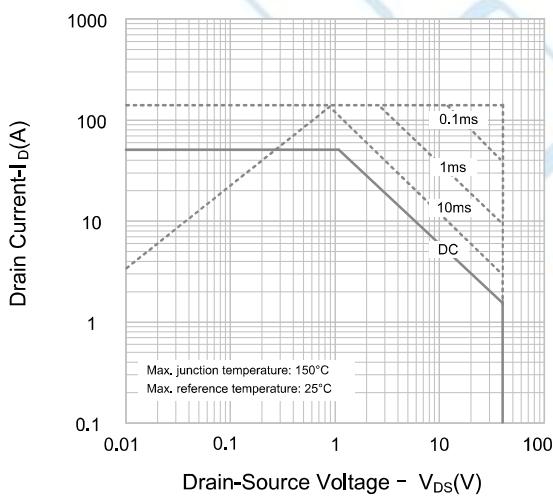
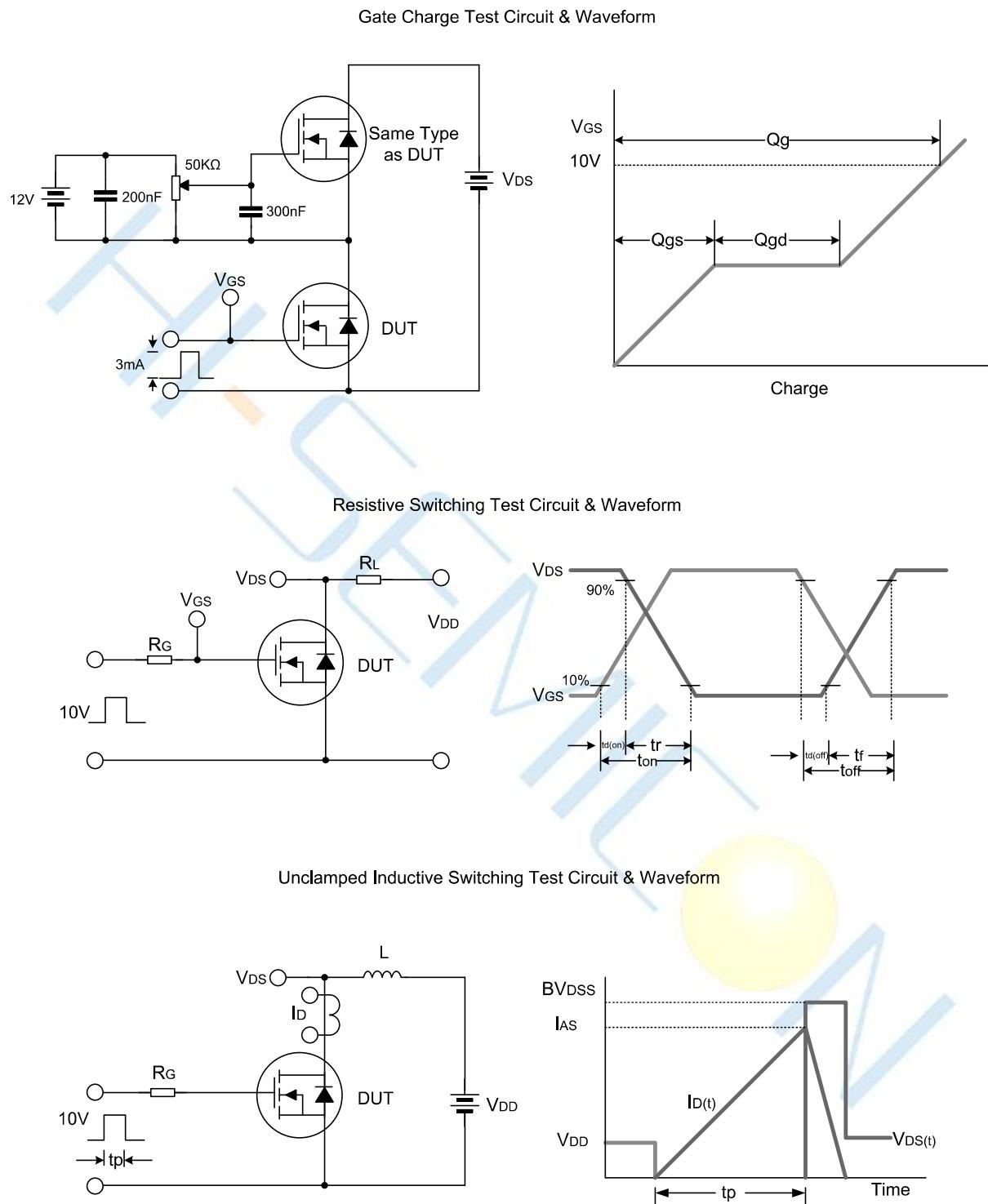


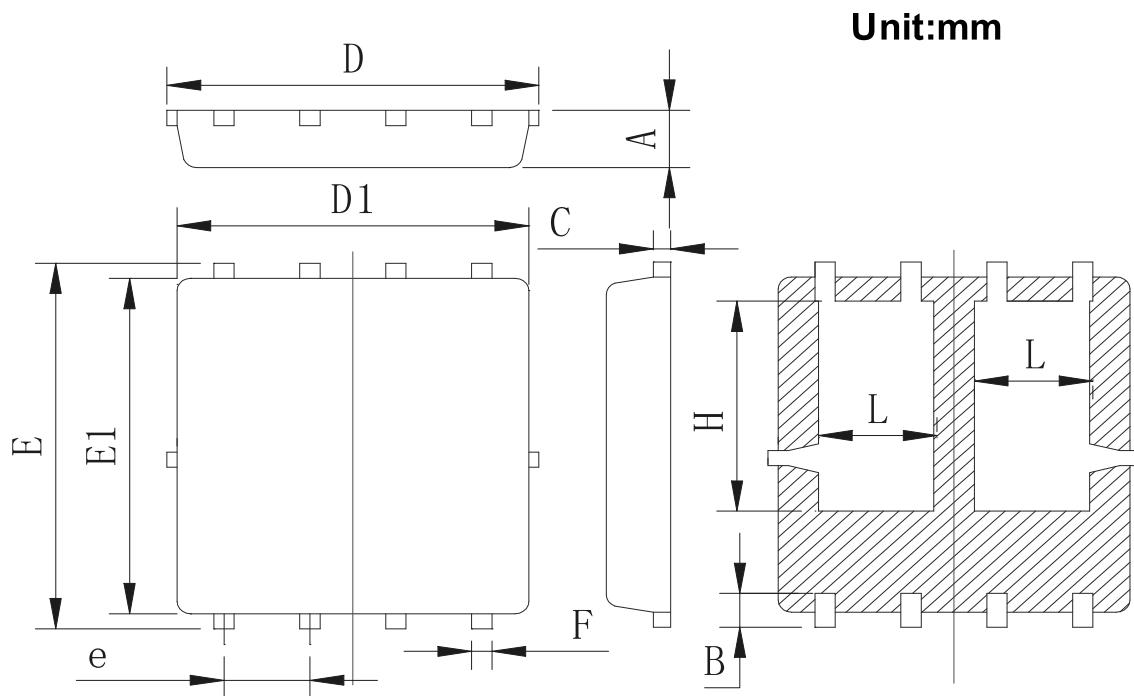
Figure 9.Max.Safe Operating Area



## Test Circuit



## Package Dimensions of PDFN5\*6-8L



| Symbol | Min   | Typ   | Max   |
|--------|-------|-------|-------|
| A      | 0.90  | 0.95  | 1.00  |
| B      | 0.48  | 0.58  | 0.68  |
| C      | 0.234 | 0.254 | 0.282 |
| D      | 5.40  | 5.50  | 5.60  |
| D1     | 5.10  | 5.20  | 5.30  |
| E      | 5.95  | 6.05  | 6.15  |
| E1     | 5.45  | 5.55  | 5.65  |
| e      | 1.22  | 1.27  | 1.32  |
| F      | 0.25  | 0.30  | 0.35  |
| H      | 3.37  | 3.47  | 3.57  |
| L      | 1.60  | 1.70  | 1.80  |

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